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Terms	Documents
L3 and ((configur\$3 or plac\$3) near10 bus)	29

Database:

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### Search History

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*DB=PGPB; PLUR=YES; OP=OR*L4 L3 and ((configur\$3 or plac\$3) near10 bus)

29

L4L3 l1 and L2

107

L3L2 ((determin\$3 or find\$3 or check\$3) same (balance or imbalance or congestion))

30975

L2L1 monitor\$3 same (activity or utiliz\$5) same bus

1864

L1

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L4: Entry 1 of 29

File: PGPB

Mar 1, 2007

PGPUB-DOCUMENT-NUMBER: 20070050653

PGPUB-FILING-TYPE:

DOCUMENT-IDENTIFIER: US 20070050653 A1

TITLE: System and method for information handling system adaptive variable bus idle timer

PUBLICATION-DATE: March 1, 2007

## INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY
Verdun; Gary	Georgetown	TX	US

APPL-NO: 11/215260 [PALM]

DATE FILED: August 29, 2005

## INT-CL-PUBLISHED:

TYPE	IPC	DATE	IPC-OLD
IPCP	G06F1/26	20060101	G06F001/26

## INT-CL-CURRENT:

TYPE	IPC	DATE
CIPP	G06 F 1/26	20060101

US-CL-PUBLISHED: 713/320

US-CL-CURRENT: 713/320

## ABSTRACT:

Power management of an information handling system PCI Express bus dynamically adjusts the inactivity time at the bus that is determined before initiation of a low power state by analyzing the transitions between low power and operating states over time. Dwell times of the bus in the low power state are compared with an inactivity goal to determine if the inactivity time should be adjusted up, such as when the bus enters the low power state too often, or should be adjusted down, such as when the bus enters the low power state too infrequently. In one embodiment, the dwell time is the time from entry into a low power state until initiation of the transition to an operating state and the inactivity goal is the time required for the bus to enter and exit the low power state.

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### Search Results -

Terms	Documents
L7 and ((configur\$3 or plac\$3) near10 bus)	52

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<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>			
<u>L8</u>	L7 and ((configur\$3 or plac\$3) near10 bus)	52	<u>L8</u>
<u>L7</u>	l5 and L6	260	<u>L7</u>
<u>L6</u>	((determin\$3 or find\$3 or check\$3) same (balance or imbalance or congestion))	136284	<u>L6</u>
<u>L5</u>	monitor\$3 same (activity or utiliz\$5) same bus	6721	<u>L5</u>
<i>DB=PGPB; PLUR=YES; OP=OR</i>			
<u>L4</u>	L3 and ((configur\$3 or plac\$3) near10 bus)	29	<u>L4</u>
<u>L3</u>	l1 and L2	107	<u>L3</u>
<u>L2</u>	((determin\$3 or find\$3 or check\$3) same (balance or imbalance or congestion))	30975	<u>L2</u>
<u>L1</u>	monitor\$3 same (activity or utiliz\$5) same bus	1864	<u>L1</u>

END OF SEARCH HISTORY.

## Refine Search

### Search Results -

Terms	Documents
(370/420   370/437   370/354   370/229   370/235   370/237   709/235   709/239   709/240   710/305   710/306   710/107   710/309   710/316   711/147   713/300   713/320   714/47).ccls.	17255

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SetName Query

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*DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR*

L9 710/305,306,107,309,316;713/300,320;714/47;709/235,239,240;370/420,437,354,229,235,237;71

L8 L7 and ((configur\$3 or plac\$3) near10 bus)

L7 l5 and L6

L6 ((determin\$3 or find\$3 or check\$3) same (balance or imbalance or congestion))

L5 monitor\$3 same (activity or utiliz\$5) same bus

*DB=PGPB; PLUR=YES; OP=OR*

L4 L3 and ((configur\$3 or plac\$3) near10 bus)

L3 l1 and L2

L2 ((determin\$3 or find\$3 or check\$3) same (balance or imbalance or congestion))

L1 monitor\$3 same (activity or utiliz\$5) same bus

END OF SEARCH HISTORY

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### Search Results -

Terms	Documents
L8 and L9	2

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L10



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*DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR*

L10 L8 and L9

L9 710/305,306,107,309,316;713/300,320;714/47;709/235,239,240;370/420,437,354,229,235,237;71

L8 L7 and ((configur\$3 or plac\$3) near10 bus)

L7 L5 and L6

L6 ((determin\$3 or find\$3 or check\$3) same (balance or imbalance or congestion))

L5 monitor\$3 same (activity or utiliz\$5) same bus

*DB=PGPB; PLUR=YES; OP=OR*

L4 L3 and ((configur\$3 or plac\$3) near10 bus)

L3 L1 and L2

L2 ((determin\$3 or find\$3 or check\$3) same (balance or imbalance or congestion))

L1 monitor\$3 same (activity or utiliz\$5) same bus

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Results for "( ( monitor\* &lt;in&gt;metadata ) &lt;and&gt; ( activity &lt;in&gt;metadata ) ) &lt;and&gt; ( bus &lt;in&gt;..."

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## » Key

IEEE JNL IEEE Journal or Magazine

IET JNL IET Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IET CNF IET Conference Proceeding

IEEE STD IEEE Standard

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- ☐ 1. **Human activities monitoring at bus stops**  
Gasserm G; Bird, N.; Masoud, O.; Papanikolopoulos, N.;  
[Robotics and Automation, 2004. Proceedings. ICRA '04. 2004 IEEE Internation](#)  
Volume 1, 2004 Page(s):90 - 95 Vol.1  
Digital Object Identifier 10.1109/ROBOT.2004.1307134  
[AbstractPlus](#) | Full Text: [PDF\(628 KB\)](#) IEEE CNF  
[Rights and Permissions](#)
- ☐ 2. **Trace driven modelling and performance evaluation of tightly coupled multi systems**  
Luc, K.-Q.; Ong, S.; Hu, E.C.;  
[Computer Design: VLSI in Computers and Processors, 1988. ICCD '88.. Proceed](#)  
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Digital Object Identifier 10.1109/ICCD.1988.25756  
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- ☐ 3. **Detection of loitering individuals in public transportation areas**  
Bird, N.D.; Masoud, O.; Papanikolopoulos, N.P.; Isaacs, A.;  
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- ☐ 4. **Hardware evaluation of low power communication mechanisms for trans architectures**  
Pionteck, T.; Garcia, A.; Kabulepa, L.D.; Glesner, M.;  
[Rapid Systems Prototyping, 2003. Proceedings. 14th IEEE International Works](#)  
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- ☐ 5. **A bandwidth-sensitive update scheduling method for Internet push**  
Huang, Y.-W.; Yu, P.S.;  
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- ☐ **6. An Advanced Educational Microprocessor System**  
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[Micro, IEEE](#)  
Volume 11, Issue 1, Feb. 1991 Page(s):22 - 25, 78-9  
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- ☐ **7. Seamless Hardware/Software Performance Co-Monitoring in a Codesign : Environment with RTOS Support**  
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- ☐ **8. Aquaculture Feed Buoy Control - Part 1: System Controller**  
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Digital Object Identifier 10.1109/OCEANS.2006.307132  
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- ☐ **9. IVHS applications in Australia**  
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- ☐ **10. Some popular problems of substation automation in China**  
Luo Haiyun; Chen Gangjie; Cao Fangmei; Ge Liang; Ren Yanming; Yang-Qixu  
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Digital Object Identifier 10.1109/ICPST.2000.900044  
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- ☐ **11. Performance analysis using a non-invasive instruction trace mechanism**  
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5-7 Feb. 1997 Page(s):308 - 314  
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- ☐ **12. Interpreting the UHF signals produced by partial discharge activity in GIS**  
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Instrumentation and Measurement, IEEE Transactions on

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Trace driven modelling and performance evaluation of tightly coupled multiprocessor systems

Luc, K.-Q. Ong, S. Hu, E.C.  
IBM Thomas J. Watson Res. Center, Yorktown Heights, NY, USA;  
This paper appears in: Computer Design: VLSI in Computers and Processors, 1988. ICCD '88., Proceedings of the 1988 IEEE International Conference on  
Publication Date: 3-5 Oct. 1988  
On page(s): 533 - 536  
Meeting Date: 10/03/1988 - 10/05/1988  
Location: Rye Brook, NY  
INSPEC Accession Number: 3305166  
Digital Object Identifier: 10.1109/ICCD.1988.25756  
Posted online: 2002-08-06 16:01:25.0

Abstract

The development and comparison of two trace-driven simulation models for microsystems with tightly coupled, shared-bus multiprocessors is presented. One model monitors the complete activities of processors, private caches, global bus, and main memory, while the other first abstracts local bus activities of processors, and then takes care of the multiprocessing interaction. The second model provides a means to move more quickly within the design space, while the first model can be used to select a final optimized design. Examples are presented to illustrate how these simulation models can help a complex choice among architectures, system configurations, and chip parameters for the design of an optimized microsystem

Index Terms  
Inspec

Controlled Indexing  
digital simulation parallel architectures performance evaluation

Non-controlled Indexing

local bus activities microsystems multiprocessing interaction performance evaluation  
shared-bus multiprocessors tightly coupled multiprocessor systems trace-driven simulation models

Author Keywords  
Not Available

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	60	reserve bus	US-PGPUB; USPAT; EPO; JPO	ADJ	OFF	2007/05/30 10:42
L2	4280	alternate path	US-PGPUB; USPAT; EPO; JPO	ADJ	OFF	2007/05/30 10:43
L3	19	arbitrat\$4 same l2	US-PGPUB; USPAT; EPO; JPO	ADJ	OFF	2007/05/30 10:45
L4	9980	second bus	US-PGPUB; USPAT; EPO; JPO	ADJ	OFF	2007/05/30 10:45
L5	3260	different bus	US-PGPUB; USPAT; EPO; JPO	ADJ	OFF	2007/05/30 10:45
L6	67	grant\$4 same l5	US-PGPUB; USPAT; EPO; JPO	ADJ	OFF	2007/05/30 10:47
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L8	2503	bus a	US-PGPUB; USPAT; EPO; JPO	ADJ	OFF	2007/05/30 10:48
L9	1276	l7 and l8	US-PGPUB; USPAT; EPO; JPO	ADJ	OFF	2007/05/30 10:48
L10	207	grant and l9	US-PGPUB; USPAT; EPO; JPO	ADJ	OFF	2007/05/30 10:48
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L12	131	load and l10	US-PGPUB; USPAT; EPO; JPO	ADJ	OFF	2007/05/30 10:52
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